

## Standard Avalanche Sinterglass Diode

### Features

- Glass passivated junction
- Hermetically sealed package

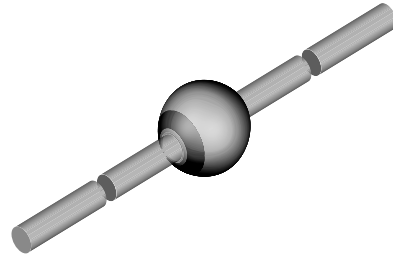
### Applications

High voltage rectification  
Efficiency diode in horizontal deflection circuits

### Mechanical Data

**Case:** Sintered glass case, SOD 64

**Terminals:** Plated axial leads, solderable per MIL-STD-750, Method 2026



949588

**Polarity:** Color band denotes cathode end

**Weight:** 860 mg, (max. 1000 mg)

**Mounting Position:** Any

### Parts Table

Part	Type differentiation	Package
BY228	$V_R = 1500\text{ V}$ ; $I_{FAV} = 3\text{ A}$	SOD64

### Absolute Maximum Ratings

$T_{amb} = 25\text{ °C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Reverse voltage	see electrical characteristics		$V_R$	1500	V
Repetitive peak reverse voltage	$I_R = 100\ \mu\text{A}$		$V_{RRM}$	1650	V
Peak forward surge current	$t_p = 10\text{ ms}$ , half sinewave		$I_{FSM}$	50	A
Average forward current			$I_{FAV}$	3	A
Junction temperature			$T_j$	140	°C
Storage temperature range			$T_{stg}$	-55 to +175	°C
Non repetitive reverse avalanche energy	$I_{(BR)R} = 0.4\text{ A}$		$E_R$	10	mJ

### Maximum Thermal Resistance

$T_{amb} = 25\text{ °C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Junction ambient	on PC board with spacing 25 mm		$R_{thJA}$	70	K/W

### Electrical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Min	Typ.	Max	Unit
Forward voltage	$I_F = 5\text{ A}$		$V_F$			1.5	V
Reverse current	$V_R = 1500\text{ V}$		$I_R$		2	5	$\mu\text{A}$
	$V_R = 1500\text{ V}, T_j = 140\text{ }^{\circ}\text{C}$		$I_R$			140	$\mu\text{A}$
Total Reverse recovery time	$I_F = 1\text{ A}, -di_F/dt = 0.05\text{ A}/\mu\text{s}$		$t_{rr}$			20	$\mu\text{s}$
Reverse recovery time	$I_F = 0.5\text{ A}, I_R = 1\text{ A}, i_R = 0,25\text{ A}$		$t_{rr}$			2	$\mu\text{s}$

### Typical Characteristics ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

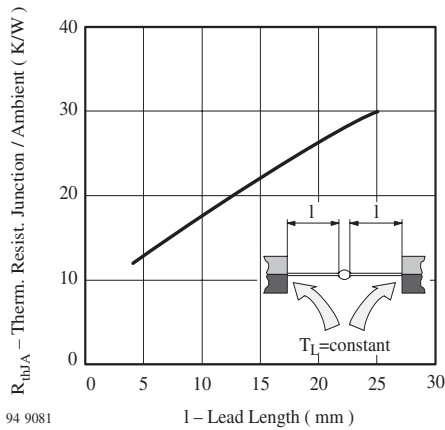


Figure 1. Typ. Thermal Resistance vs. Lead Length

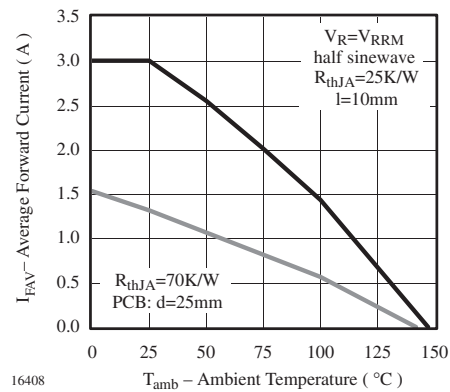


Figure 3. Max. Average Forward Current vs. Ambient Temperature

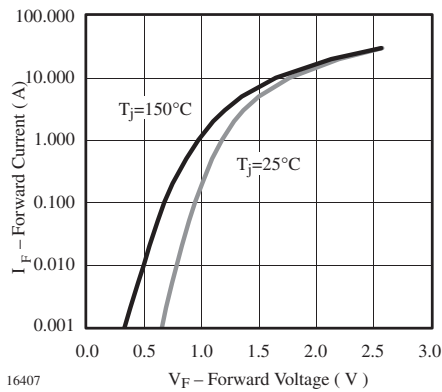


Figure 2. Forward Current vs. Forward Voltage

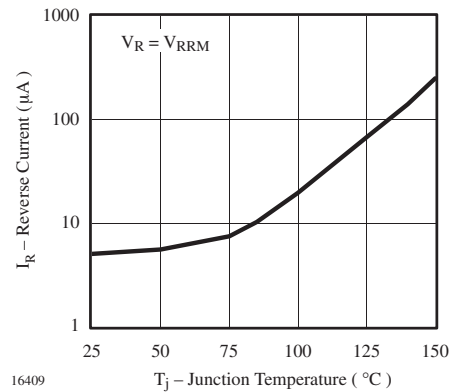


Figure 4. Reverse Current vs. Junction Temperature

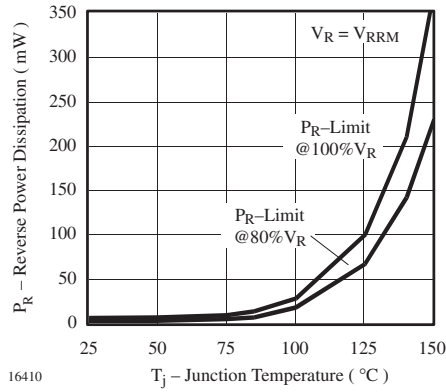


Figure 5. Max. Reverse Power Dissipation vs. Junction Temperature

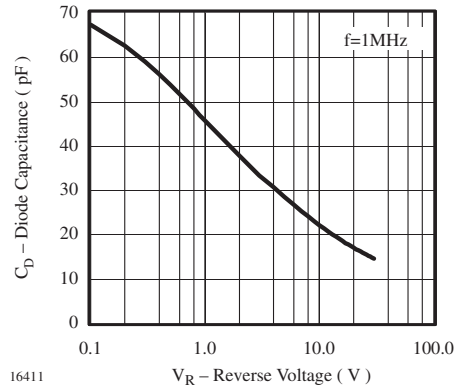
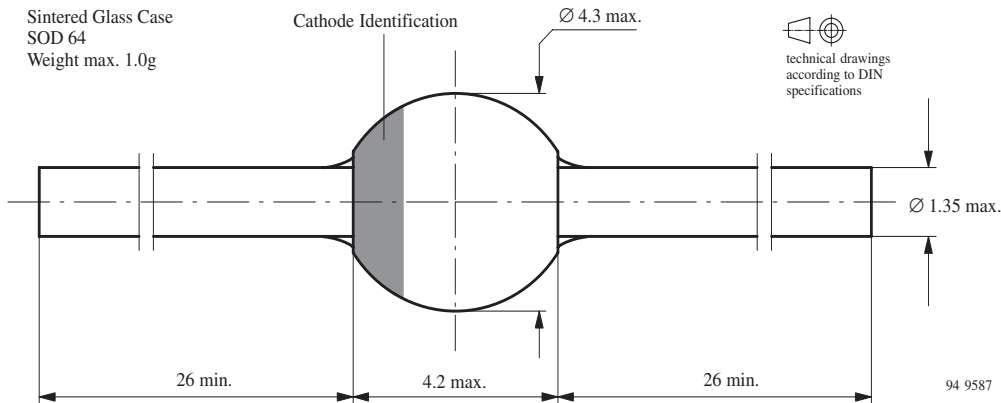


Figure 6. Diode Capacitance vs. Reverse Voltage

## Package Dimensions in mm



### Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**Vishay Semiconductor GmbH** has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**Vishay Semiconductor GmbH** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design  
and may do so without further notice.**

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